

AP2305

P-Channel Power MOSFET

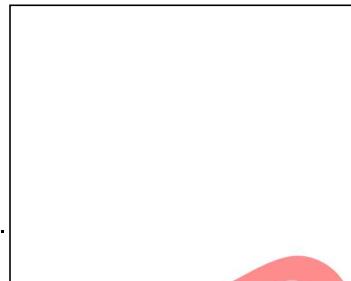
描述 / Descriptions

SOT-23 塑封封装 P 道 MOS 场效应管。P- CHANNEL MOSFET in a SOT-23 Plastic Package.

特征 / Features

沟道场效应管,MOS 场效应管。

Trench FET Power MOSFET 100% Rg Tested.

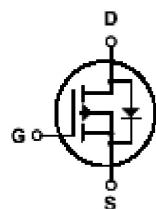


用途 / Applications

适用于作负载开关或脉宽调制应用。

This device is suitable for use as a load switch or in PWM applications.

内部等效电路 / Equivalent Circuit



引脚排列 / Pinning



Maximum ratings ($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 12	
Continuous Drain Current	I_D	-3.5	A
Continuous Source-Drain Diode Current	I_S	-0.8	
Maximum Power Dissipation	P_D	0.35	W
Thermal Resistance from Junction to Ambient($t \leq 10\text{s}$)	$R_{\theta JA}$	357	°C/W
Junction Temperature	T_J	150	°C
Storage Temperature	T_{STG}	-50 ~ +150	

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$T_a=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Static						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	-20			V
Gate-source threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	-0.5		-0.9	
Gate-source leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 8\text{V}$			± 100	nA
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}} = -8\text{V}, V_{\text{GS}} = 0\text{V}$			-1	μA
Drain-source on-state resistance ^a	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -4.5\text{V}, I_D = -3.5\text{A}$		55	65	$\text{m}\Omega$
		$V_{\text{GS}} = -2.5\text{V}, I_D = -3\text{A}$		65	80	
		$V_{\text{GS}} = -1.8\text{V}, I_D = -2.0\text{A}$		90	110	
Forward transconductance ^a	g_{fs}	$V_{\text{DS}} = -5\text{V}, I_D = -4.1\text{A}$	6			S
Dynamic						
Input capacitance ^{b,c}	C_{iss}	$V_{\text{DS}} = -4\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		740		pF
Output capacitance ^{b,c}	C_{oss}			290		
Reverse transfer capacitance ^{b,c}	C_{rss}			190		
Total gate charge ^b	Q_g	$V_{\text{DS}} = -4\text{V}, V_{\text{GS}} = -4.5\text{V}, I_D = -4.1\text{A}$		7.8	15	nC
		$V_{\text{DS}} = -4\text{V}, V_{\text{GS}} = -2.5\text{V}, I_D = -4.1\text{A}$		4.5	9	
Gate-source charge ^b	Q_{gs}	$V_{\text{DS}} = -4\text{V}, V_{\text{GS}} = -2.5\text{V}, I_D = -4.1\text{A}$		1.2		
Gate-drain charge ^b	Q_{gd}			1.6		
Gate resistance ^{b,c}	R_g	$f = 1\text{MHz}$	1.4	7	14	Ω
Turn-on delay time ^{b,c}	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = -4\text{V}, R_L = 1.2\Omega, I_D \approx -3.3\text{A}, V_{\text{GEN}} = -4.5\text{V}, R_g = 1\Omega$		13	20	ns
Rise time ^{b,c}	t_r			35	53	
Turn-off Delay time ^{b,c}	$t_{\text{d}(\text{off})}$			32	48	
Fall time ^{b,c}	t_f			10	20	
Turn-on delay time ^{b,c}	$t_{\text{d}(\text{on})}$			5	10	
Rise time ^{b,c}	t_r	$V_{\text{DD}} = -4\text{V}, R_L = 1.2\Omega, I_D \approx -3.3\text{A}, V_{\text{GEN}} = -8\text{V}, R_g = 1\Omega$		11	17	
Turn-off delay time ^{b,c}	$t_{\text{d}(\text{off})}$			22	33	
Fall time ^{b,c}	t_f			16	24	
Drain-source body diode characteristics						
Continuous source-drain diode current	I_s	$T_C = 25^\circ\text{C}$			-1.4	A
Pulse diode forward current ^a	I_{SM}				-10	
Body diode voltage	V_{SD}	$I_F = -3.3\text{A}$			-1.2	V

Note :

- a. Pulse Test ; Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
- b. Guaranteed by design, not subject to production testing.
- c. These parameters have no way to verify.