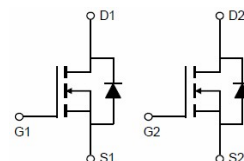


Feature

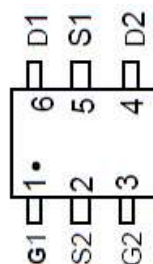
- 40V,5A
 $R_{DS(ON)} < 36m\Omega @ V_{GS}=4.5V$ TYP:30 m Ω
 $R_{DS(ON)} < 45m\Omega @ V_{GS}=2.5V$ TYP:35 m Ω
- Advanced Trench Technology
- Lead free product is acquired

Application

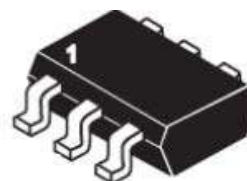
- Interfacing Switching
- Load Switching
- Power management



Schematic diagram



Marking and pin Assignment



SOT23-6 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity (PCS)
6900	AP6900	Sot-23-6	7 inch	-	3000

ABSOLUTE MAXIMUM RATINGS ($T_a=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($T_a=25^{\circ}\text{C}$)	I_D	5	A
Continuous Drain Current ($T_a=70^{\circ}\text{C}$)	I_D	3	A
Pulsed Drain Current	I_{DM}	20	A
Power Dissipation	P_D	1.6	W
Thermal Resistance from Junction to Ambient ⁽⁴⁾	$R_{\theta JA}$	78	$^{\circ}\text{C/W}$
Junction Temperature	T_J	150	$^{\circ}\text{C}$
Storage Temperature	T_{STG}	-55~ +150	$^{\circ}\text{C}$

MOSFET ELECTRICAL CHARACTERISTICS($T_a=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D =250μA	40	-	-	V
Zero gate voltage drain current	I _{DSS}	V _{DS} =40V, V _{GS} = 0V	-	-	1	μA
Gate-body leakage current	I _{GSS}	V _{GS} =±20V,V _{DS} = 0V	-	-	±2000	nA
Gate threshold voltage ⁽³⁾	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1.0	1.4	2.5	V
Drain-source on-resistance ⁽³⁾	R _{DS(on)}	V _{GS} =10V, I _D =4A	-	30	36	mΩ
		V _{GS} =4.5V, I _D =3A	-	35	45	
Dynamic characteristics						
Input Capacitance	C _{iss}	V _{DS} =20V, V _{GS} =0V, f =1MHz	-	435	-	pF
Output Capacitance	C _{oss}		-	58	-	
Reverse Transfer Capacitance	C _{rss}		-	35	-	
Switching characteristics						
Turn-on delay time	t _{d(on)}	V _{DD} =20V, I _D =4A, V _{GS} =10V, R _G =3Ω	-	10	-	ns
Turn-on rise time	t _r		-	8	-	
Turn-off delay time	t _{d(off)}		-	29	-	
Turn-off fall time	t _f		-	12	-	
Total Gate Charge	Q _g	V _{DS} =20V, I _D =3A, V _{GS} =10V	-	11	-	nC
Gate-Source Charge	Q _{gs}		-	2	-	
Gate-Drain Charge	Q _{gd}		-	2.5	-	
Source-Drain Diode characteristics						
Diode Forward voltage ⁽³⁾	V _{DS}	V _{GS} =0V, I _S =4A	-	-	1.2	V
Diode Forward current ⁽⁴⁾	I _S		-	-	5.0	A

Notes:

1. Repetitive Rating: pulse width limited by maximum junction temperature
2. Pulse Test: pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
3. Surface Mounted on FR4 Board, $t \leq 10$ sec

Test Circuit

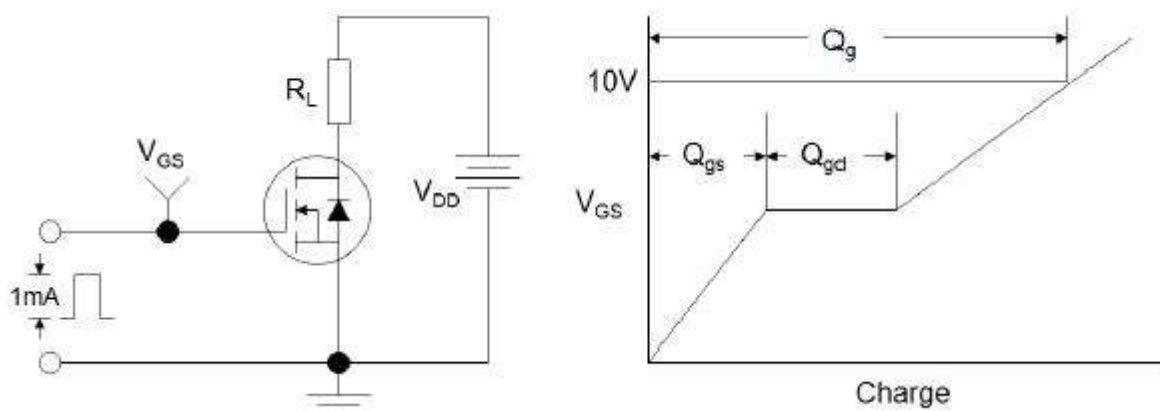


Figure1:Gate Charge Test Circuit & Waveform

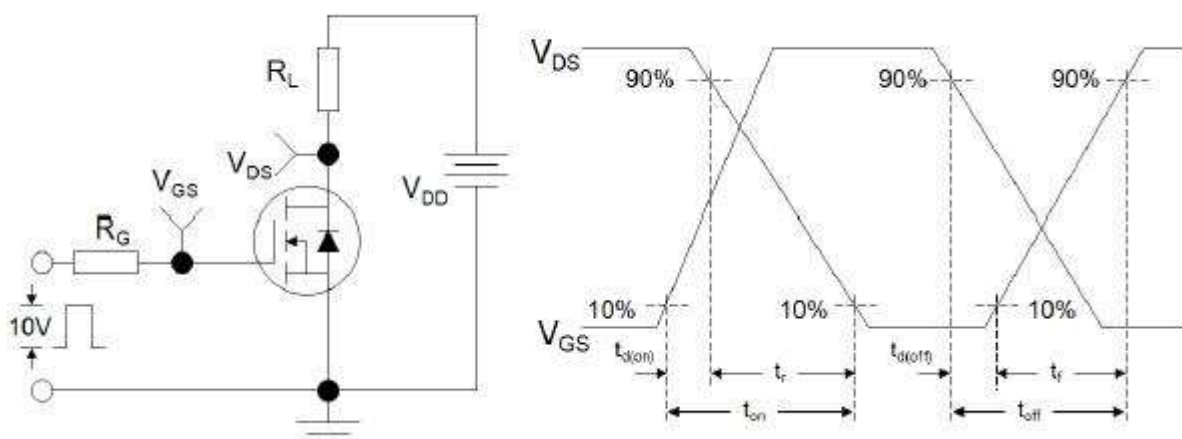


Figure 2: Resistive Switching Test Circuit & Waveforms

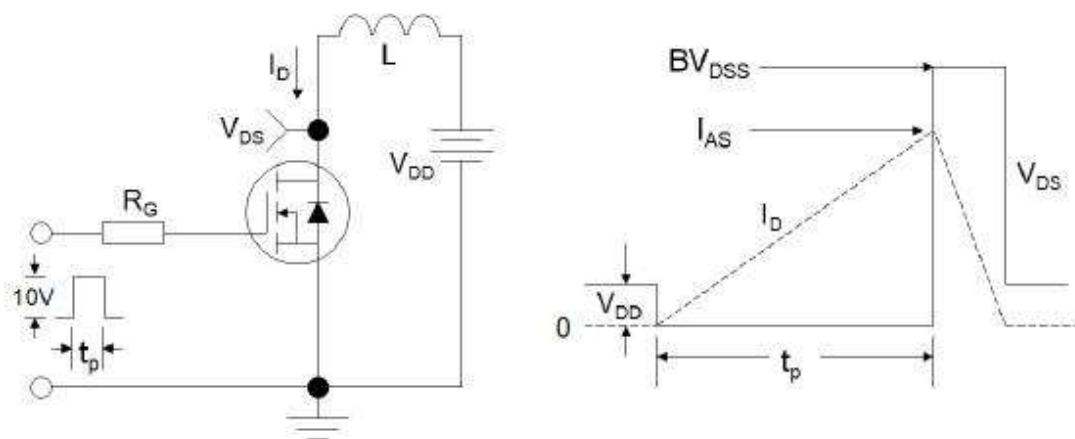


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

Typical Performance Characteristics

Figure1: Output Characteristics

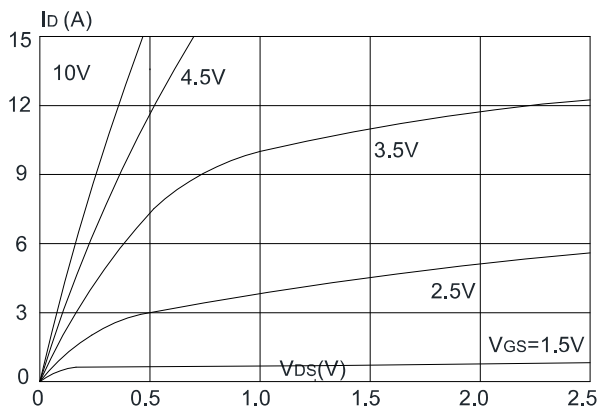


Figure 2: Typical Transfer Characteristics

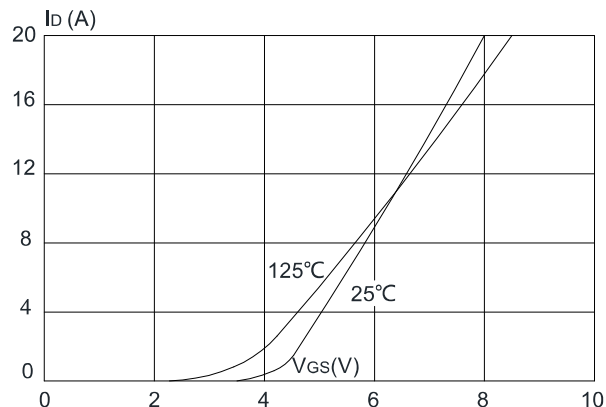


Figure 3: On-resistance vs. Drain Current

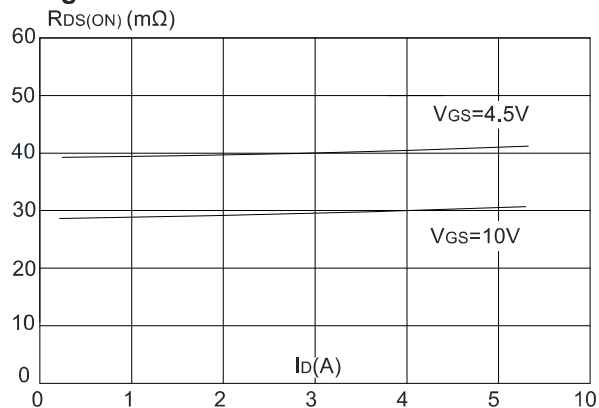


Figure 4: Body Diode Characteristics

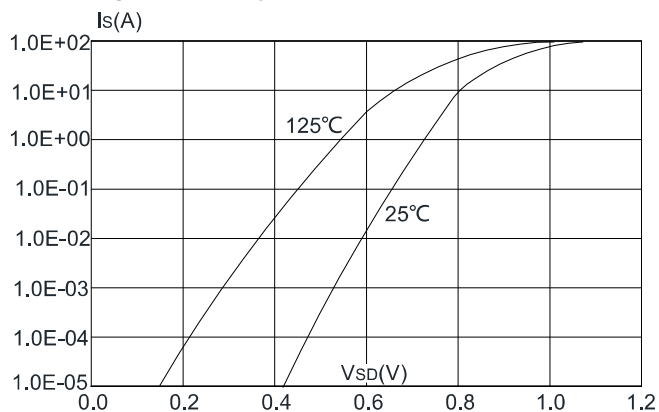


Figure 5: Gate Charge Characteristics

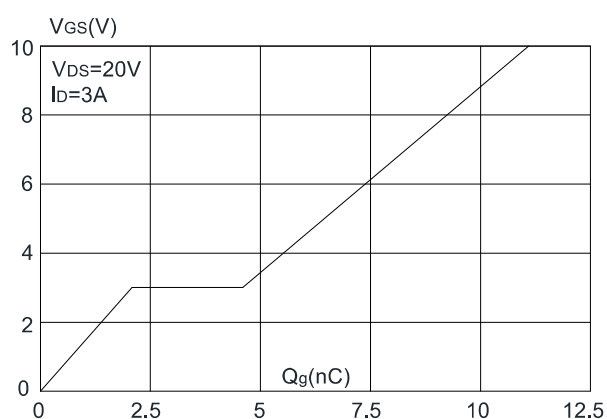


Figure 6: Capacitance Characteristics

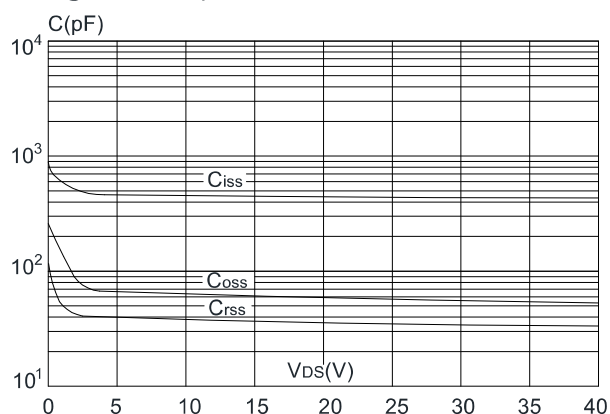


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

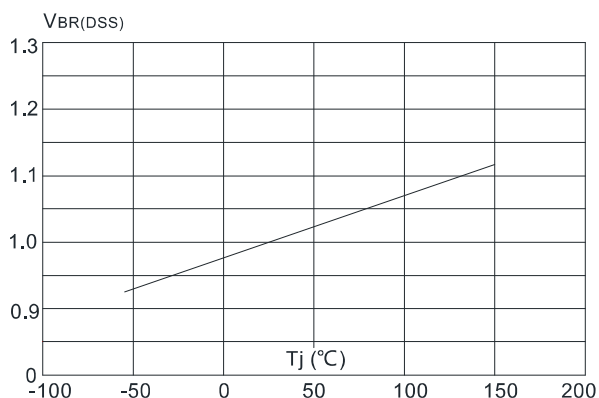


Figure 8: Normalized on Resistance vs. Junction Temperature

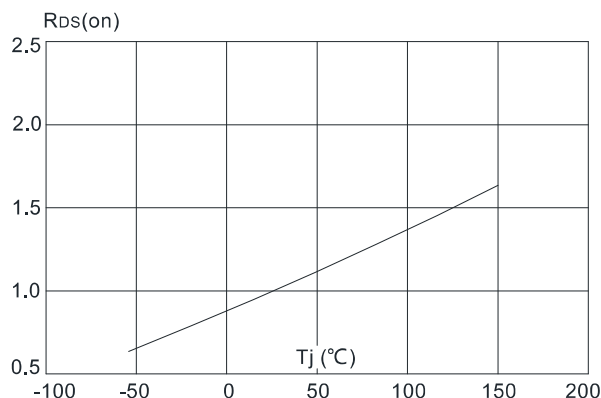


Figure 9: Maximum Safe Operating Area

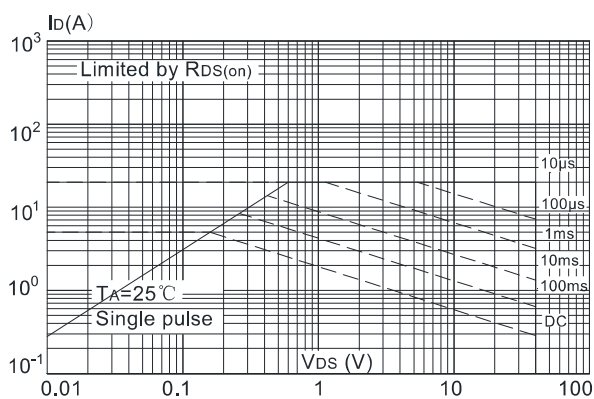


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

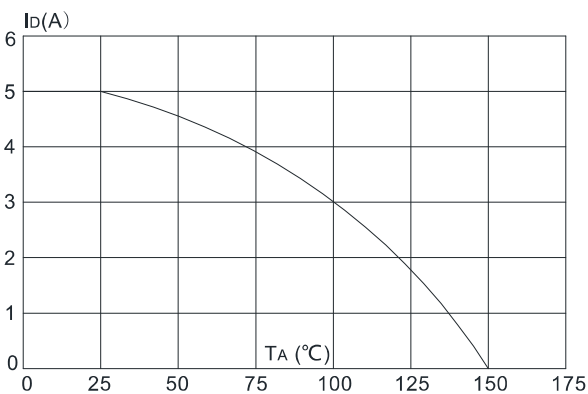
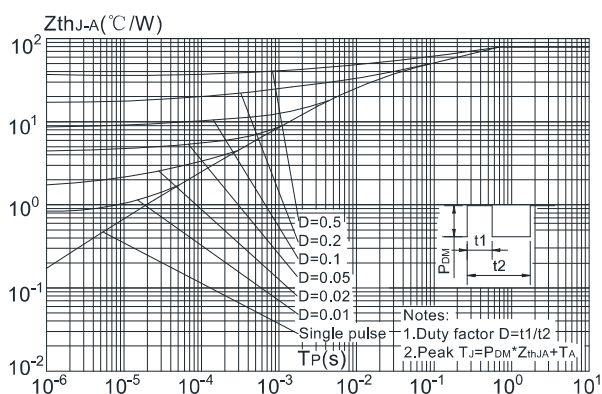
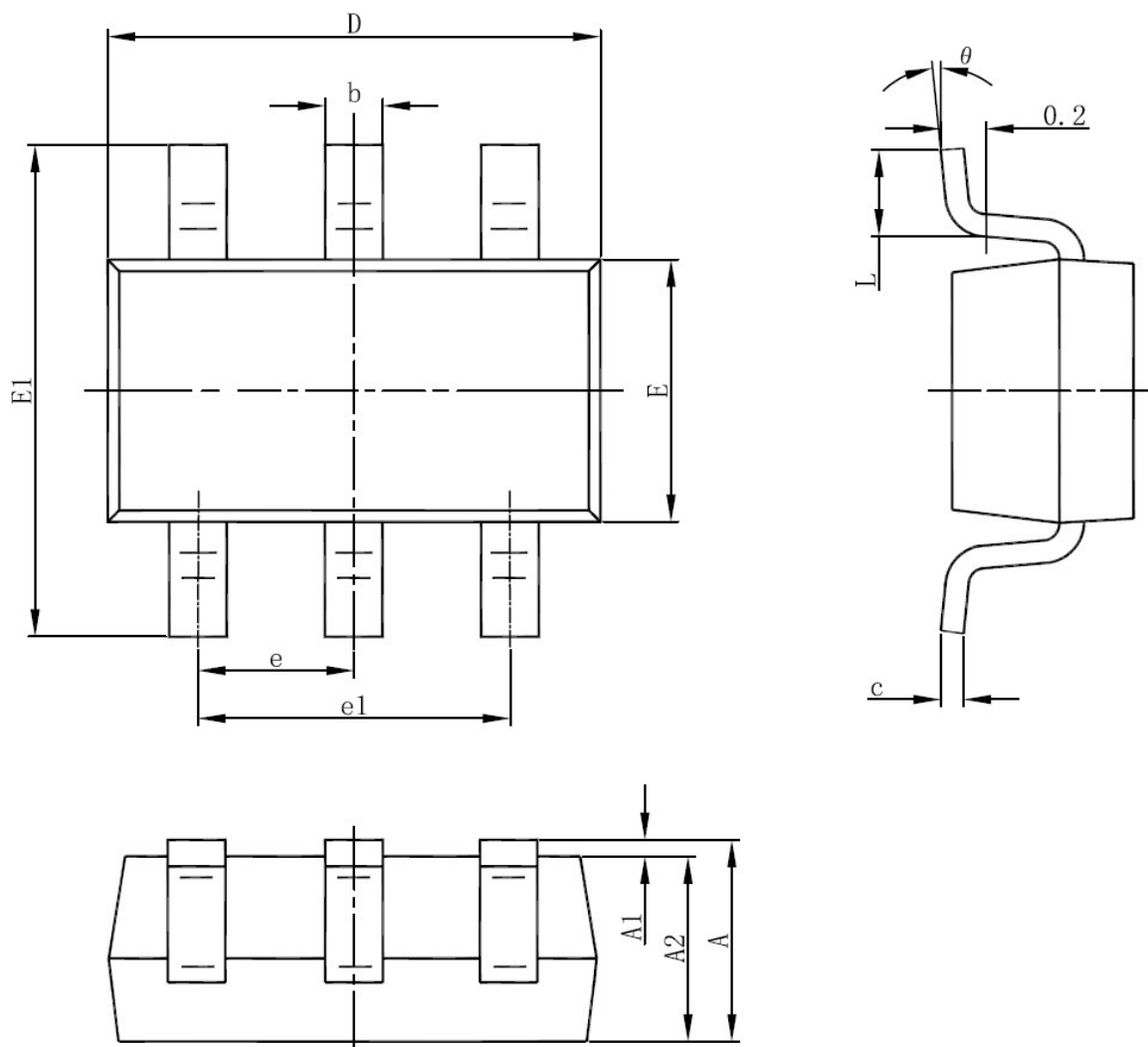


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



SOT23-6 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°