

40V, 1.5MHz, Rail-to-Rail Input/Output Single Supply Operational Amplifiers

Features

Operates on

Single-Supply: $3.0V \sim 40V$, or

Dual-Supply: ±1.5V ~ ±20V

■ Low Quiescent Current: 170µA

■ Gain Bandwidth Product: 1.5MHz

Rail-to-Rail Input and Output (RRIO)

■ 1.0V/µs Slew Rate

■ Low offset drift: ±0.3µV/°C (Typical)

Unity Gain Stable

■ No Phase Reversal

RFI Filtered Inputs

Low-input Bias current: 10pA

■ Extended Temperature Ranges
From -40°C to +125°C

Small Packaging
 COS170 available in SOT23-5/SOP-8
 COS2170 available in SOP-8/MSOP-8
 COS4170 available in SOP14/TSSOP14

Applications

- Automotive Electronics
- Sensor Conditioning
- AGC Control
- Tracking Amplifier in Power Modules
- Battery-Powered Instruments
- Test Equipment

General Description

The COS170 (single), COS2170 (dual) and COS4170 (quad) are single supply, rail-to-rail input and output amplifiers operated on 3.0V(±1.5V) to 40V(±20V) supplies. Despite their low quiescent current, the COSx170 family provides excellent overall performance and versatility.

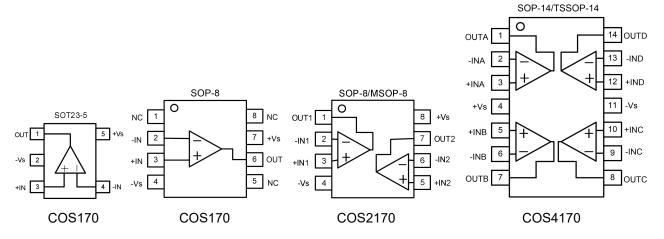
COSx170 family is unity gain stable and has a gain bandwidth product of 1.5MHz (typical). high CMRR and PSRR They provide performance and can operate from a single supply or dual supply. Input signals can go beyond the supply rails and do not cause phase reversal. The output voltage swing extends to within 150mV of each rail, providing the maximum output dynamic range with excellent overdrive recovery. These features make the COSx170 family well suited for single-supply, battery-powered applications. They can be used as plus-in replacements for many commercially available op-amps to reduce power and improve input/output range and performance.

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1. Pin Configuration and Functions



Pin Functions

Name	Description	Note
+Vs	Positive power supply	A bypass capacitor of 0.1µF as close to the part as possible should be placed between power supply pins or between supply pins and ground.
-Vs	Negative power supply or ground	If it is not connected to ground, bypass it with a capacitor of 0.1µF as close to the part as possible.
-IN	Negative input	Inverting input of the amplifier. Voltage range of this pin can go from -Vs -0.3V to +Vs + 0.3V.
+IN	Positive input	Non-inverting input of the amplifier. This pin has the same voltage range as –IN.
OUT	Output	The output voltage range extends to within millivolts of each supply rail.
NC	No connection	No internal connection, can be left floating

2. Package and Ordering Information

Model	Channel	Order Number	Package	Package Option	Marking Information
		COS170TRA	SOT23-5	Tana and Book 2000	COS170
000170	4	COS170TRB	50123-5	Tape and Reel, 3000	COS170
COS170 1	I	COS170SRA	SOP-8	Tana and Book 4000	COS170SR
		COS170SRB		Tape and Reel, 4000	COS170SR
		COS2170SRA	SOP-8	Tana and Daal 4000	COS2170SR
0000170	0	COS2170SRB	30P-0	Tape and Reel, 4000	COS2170SR
COS2170	2	COS2170MRA	MSOP-8	T	COS2170MR
		COS2170MRB		Tape and Reel, 3000	COS2170MR



COS170, COS2170, COS4170

COS4170 4	4	COS4170SR	SOP-14	Tape and Reel, 2500	COS4170SR
CO34170	4	COS4170TR	TSSOP-14	Tape and Reel, 3000	COS4170TR

3. Product Specification

3.1 Absolute Maximum Ratings (1)

Parameter	Rating	Units
Power Supply: +Vs to -Vs	42	V
Input Voltage	-Vs -0.5V to +Vs + 0.5V	V
Input Current (2)	±10	mA
Storage Temperature Range	-65 to 150	°C
Junction Temperature	150	°C
Operating Temperature Range	-40 to 125	°C
ESD Susceptibility, HBM	2000	V

⁽¹⁾ Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

3.2 Thermal Data

Parameter	Rating	Unit
Package Thermal Resistance, R _{θJA} (Juntion-to-ambient)	190 (SOT23-5) 206 (MSOP8) 155 (SOP8) 105 (TSSOP14) 82 (SOP14)	°C/W

3.3 Recommended Operating Conditions

Parameter	Rating	Unit
DC Supply Voltage	3.0 (±1.5) ~ 40 (±20)	V
Input common-mode voltage range	-Vs ~ +Vs	V
Operating ambient temperature	-40 to +85	°C

⁽²⁾ Input terminals are diode-clamped to the power-supply rails. Input signals that can swing more than 0.5V beyond the supply rails should be current-limited to 10mA or less.



3.4 Electrical Characteristics

(V_S=3.0 to 40V, V_{CM}= V_{OUT}=V_S/2, T_A =+25°C, R_L =10k Ω to V_S/2, unless otherwise noted)

Symbol	Conditions	Min	Тур	Max	Unit		
Input Characteristics							
	COS170A, COS2170A		±0.3	±2.0			
Vos	COS170B, COS2170B		±0.75	±5.0	- mV		
ΔV _{OS} /ΔT	-40 to 125°C		±0.3	±3.0	μV/°C		
IB			±10		pА		
Ios			±4		pА		
V _{CM}	V _S = 40V	V _{-S} -0.1		V _{+S} -2	V		
CMRR	V _{-S} -0.1 < V _{CM} < V _{+S} -2	100	120		dB		
AOL	V ₀ =0.2V to 4.8V	110	130		dB		
			•	•			
	R _L =10kΩ		150		mV		
I _{SR}	Sourcing		+40		mA		
I _{SK}	Sinking		-48		mA		
		3.0		40	V		
PSRR	V _S = +3.0V to +40V	80	100		dB		
IQ			170		μA		
GBWP	G=+1		1.5		MHz		
SR	G=+1		1.0		V/µs		
e _n	f=1kHz		28		nV/ √ Hz		
	IB Ios VcM CMRR AOL IsR IsK PSRR IQ GBWP SR	Vos COS170B, COS2170B ΔVos/ΔT -40 to 125°C IB Ios V _{CM} V _S = 40V CMRR V _S -0.1 < V _{CM} < V _{+S} -2 AOL V _O =0.2V to 4.8V I _{SR} Sourcing I _{SK} Sinking PSRR V _S = +3.0V to +40V I _Q GBWP GBWP G=+1 SR G=+1	Vos COS170B, COS2170B $\Delta V_{OS}/\Delta T$ -40 to 125°C IB Ios V_{CM} $V_S = 40V$ $V_S = 0.1$ CMRR $V_S = 0.1 < V_{CM} < V_{+S} - 2$ 100 AOL $V_O = 0.2V$ to $4.8V$ 110 IsR Sourcing IsR IsR Sinking PSRR $V_S = +3.0V$ to $+40V$ 80 IQ GBWP G=+1 SR G=+1	Vos COS170B, COS2170B ± 0.75 $\Delta V_{OS}/\Delta T$ -40 to 125°C ± 0.3 IB ± 10 I_{OS} ± 4 V _{CM} $V_S = 40V$ $V_S = 0.1$ CMRR $V_{S} = 0.1 < V_{CM} < V_{+S} - 2$ 100 120 AOL $V_O = 0.2V$ to $4.8V$ 110 130 I _{SR} Sourcing $+40$ I _{SR} Sinking -48 PSRR $V_S = +3.0V$ to $+40V$ 80 100 I _Q 170 GBWP $G = +1$ 1.5 SR $G = +1$ 1.0	Vos COS170B, COS2170B ±0.75 ±5.0 ΔVos/ΔT -40 to 125°C ±0.3 ±3.0 IB ±10 Ios ±4 V _{CM} V _S = 40V V _S = -0.1 V _S = -0.1 CMRR V _S -0.1 < V _{CM} < V _{+S} -2 100 120 AOL V _O =0.2V to 4.8V 110 130 I _{SR} Sourcing +40 I _{SK} Sinking -48 PSRR V _S = +3.0V to +40V 80 100 I _Q 170		



4.0 Application Notes

Driving Capacitive Loads

Driving large capacitive loads can cause stability problems for voltage feedback op amps. As the load capacitance increases, the feedback loop's phase margin decreases, and the closed loop bandwidth is reduced. This produces gain peaking in the frequency response, with overshoot and ringing in the step response. A unity gain buffer (G = +1) is the most sensitive to capacitive loads, but all gains show the same general behavior.

When driving large capacitive loads with these op amps (e.g., > 100 pF when G = +1), a small series resistor at the output (R_{ISO} in Figure 1) improves the feedback loop's phase margin (stability) by making the output load resistive at higher frequencies. It does not, however, improve the bandwidth.

To select R_{ISO} , check the frequency response peaking (or step response overshoot) on the bench. If the response is reasonable, you do not need R_{ISO} . Otherwise, start R_{ISO} at 1 k Ω and modify its value until the response is reasonable.

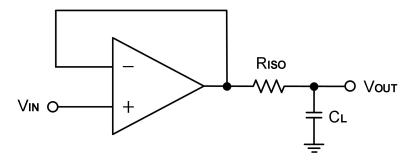


Figure 1. Indirectly Driving Heavy Capacitive Load

An improvement circuit is shown in Figure 2. It provides DC accuracy as well as AC stability. R_F provides the DC accuracy by connecting the inverting signal with the output, C_F and R_{ISO} serve to counteract the loss of phase margin by feeding the high frequency component of the output signal back to the amplifier's inverting input, thereby preserving phase margin in the overall feedback loop.

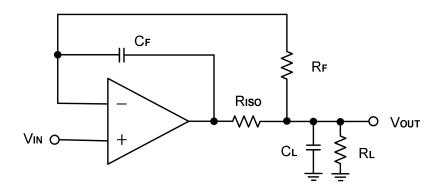


Figure 2. Indirectly Driving Heavy Capacitive Load with DC Accuracy



For non-inverting configuration, there are two others ways to increase the phase margin: (a) by increasing the amplifier's gain or (b) by placing a capacitor in parallel with the feedback resistor to counteract the parasitic capacitance associated with inverting node, as shown in Figure 3.

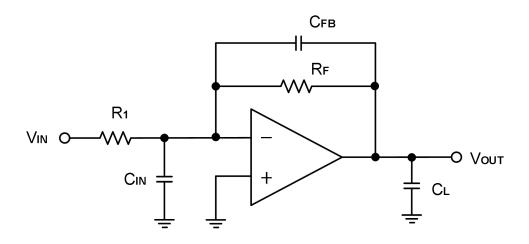


Figure 3. Adding a Feedback Capacitor in the Noninverting Configuration

Power-Supply Bypassing and Layout

The COSx170 family operates from a single +3.0V to +40V supply or dual ± 1.5 V to ± 20 V supplies. For single-supply operation, bypass the power supply +Vs with a 0.1 μ F ceramic capacitor which should be placed close to the +Vs pin. For dual-supply operation, both the +Vs and the -Vs supplies should be bypassed to ground with separate 0.1 μ F ceramic capacitors. 2.2 μ F tantalum capacitor can be added for better performance.

The length of the current path is directly proportional to the magnitude of parasitic inductances and thus the high frequency impedance of the path. High speed currents in an inductive ground return create an unwanted voltage noise. Broad ground plane areas will reduce the parasitic inductance. Thus a ground plane layer is important for high speed circuit design.

Typical Application Circuits

Differential Amplifier

The circuit shown in Figure 4 performs the differential function. If the resistors ratios are equal $(R_4 / R_3 = R_2 / R_1)$, then $V_{OUT} = (V_{IP} - V_{IN}) \times R_2 / R_1 + V_{REF}$.



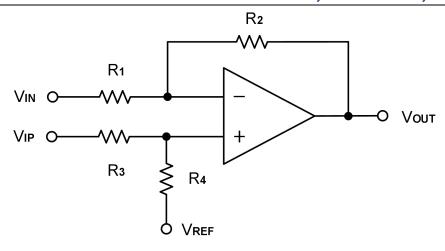


Figure 4. Differential Amplifier

Low Pass Active Filter

When receiving low-level signals, limiting the bandwidth of the incoming signals into the system is often required. The simplest way to establish this limited bandwidth is to place an RC filter at the noninverting terminal of the amplifier. If even more attenuation is needed, a multiple pole filter is required. The Sallen-Key filter can be used for this task, as Figure 5. For best results, the amplifier should have a bandwidth that is 8 to 10 times the filter frequency bandwidth. Failure to follow this guideline can result in reduction of phase margin. The large values of feedback resistors can couple with parasitic capacitance and cause undesired effects such as ringing or oscillation in high-speed amplifiers. Keep resistors value as low as possible and consistent with output loading consideration.

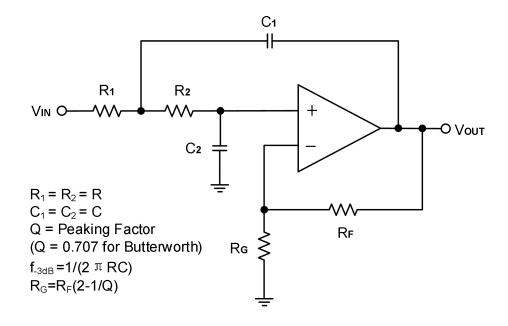
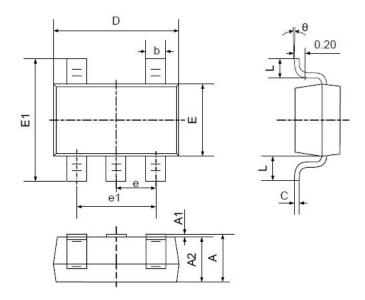


Figure 5. Two-Pole Low-Pass Sallen-Key Active Filter



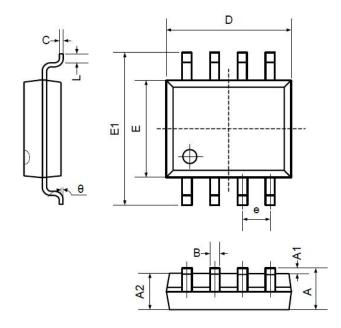
5. Package Information

5.1 SOT23-5 (Package Outline Dimensions)



Symbol		nsions imeters	Dimensions In Inches	
	Min	Max	Min	Max
Α	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.400	0.012	0.016
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
е	0.950	OTYP	0.03	7TYP
e1	1.800	2.000	0.071	0.079
L	0.700REF		0.028	BREF
L1	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

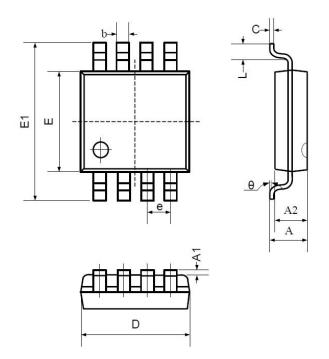
5.2 SOP8 (Package Outline Dimensions)



Symbol		nsions meters	Dimensions In Inches	
	Min	Max	Min	Max
Α	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
В	0.330	0.510	0.013	0.020
С	0.190	0.250	0.007	0.010
D	4.780	5.000	0.188	0.197
E	3.800	4.000	0.150	0.157
E1	5.800	6.300	0.228	0.248
е	1.270TYP		0.050	OTYP
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

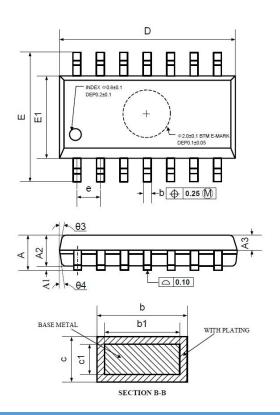


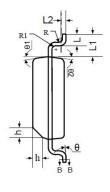
5.3 MSOP8 (Package Outline Dimensions)



Symbol		nsions meters	Dimensions In Inches	
-	Min	Max	Min	Max
Α	0.800	1.200	0.031	0.047
A1	0.000	0.200	0.000	0.008
A2	0.760	0.970	0.030	0.038
b	0.30	TYP	0.012 TYP	
С	0.15	TYP	0.006 TYP	
D	2.900	3.100	0.114	0.122
е	0.65	TYP	0.026	TYP
E	2.900	3.100	0.114	0.122
E1	4.700	5.100	0.185	0.201
L	0.410	0.650	0.016	0.026
θ	0°	6°	0°	6°

5.4 SOP14 (Package Outline Dimensions)

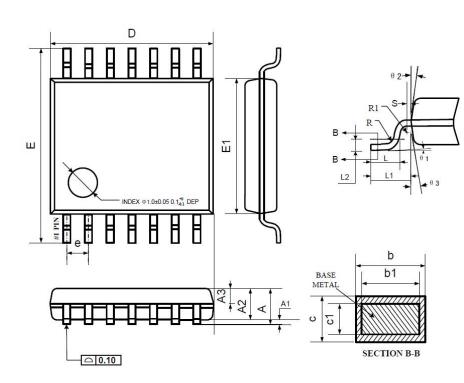




Symbol	Dimensions In Millimeters				
	MIN	NOM	MAX		
Α	1.35	1.60	1.75		
A1	0.10	0.15	0.25		
A2	1.25	1.45	1.65		
A3	0.55	0.65	0.75		
b	0.36		0.49		
b1	0.35	0.40	0.45		
С	0.16		0.25		
c1	0.15	0.20	0.25		
D	8.53	8.63	8.73		
E	5.80	6.00	6.20		
E1	3.80	3.90	4.00		
е		1.27 BS0	3		
L	0.45	0.60	0.80		
L1		1.04 REI	-		
L2		0.25 BS0	0		
R	0.07				
R1	0.07				
h	0.30	0.40	0.50		
θ	0°		8°		
91	6°	8°	10°		
02	6°	8°	10°		
93	5°	7°	9°		
94	5°	7°	9°		



5.5 TSSOP14 (Package Outline Dimensions)



Symbol	HAS THE REAL PROPERTY OF THE P					
	MIN	NOM	MAX			
Α		_	1.20			
A1	0.05	1-1	0.15			
A2	0.90	1.00	1.05			
A3	0.34	0.44	0.54			
b	0.20	-	0.28			
b1	0.20	0.22	0.24			
С	0.10	1. .:	0.19			
c1	0.10	0.13	0.15			
D	4.86	4.96	5.06			
E	6.20	6.40	6.60			
E1	4.30	4.40	4.50			
е		0.65 BSC				
L	0.45	0.60	0.75			
L1		1.00 REF				
L2		0.25 BSC				
R	0.09	·—·	_			
R1	0.09	.—.	_			
S	0.20	9 <u></u> 8				
θ1	0°	_	8°			
θ2	10°	12°	14°			
θ3	10°	12°	14°			

6. Related Parts

Part Number	Description
COS6041/2/4	24kHz, 0.5μA, RRIO Op Amps, 1.8 to 5.5V Supply
COS1347/2347/4347	350kHz, 15μA, RRIO Op Amps, 1.8 to 5.5V Supply
COS6001/2/4	1.1.2MHz, 50μA, RRIO Op Amps, 1.8 to 5.5V Supply
COS1314/2314/4314	3MHz, 150μA, RRIO Op Amps, 1.8 to 5.5V Supply
COS821/2/4	1.2MHz, 300μA, RRIO Op Amps, 1.8 to 5.5V Supply
COS1374/2374/4374	7MHz, 500μA, RRIO Op Amps, 1.8 to 5.5V Supply
COS721/2/4	10MHz, 650μA, RRIO Op Amps, 2.1 to 5.5V Supply
COS1333/2333/4333	0.31.2MHz, 18μA, RRIO Op Amps, 1.8 to 5.5V Supply, Zero Drift, Vos<20μV
COS8551/2/4	1.1.2MHz, 55μA, RRIO Op Amps, 1.8 to 5.5V Supply, Zero Drift, Vos<10μV